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/Erin Cowles/
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	10/034,528	Conf. No.:	2563
Applicant	:	Eldridge et al.		
Filed	:	December 27, 2001		
TC/A.U.	:	2829		
Examiner	:	Paresh H. Patel		
Docket No.	:	P6C3-US		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT**To Whom It May Concern:**

Listed on an attached Form PTO/SB/08A is information known to applicant(s). Applicants ask that the attached Form PTO/SB/08A be initialed and returned in accordance with MPEP §609. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in §1.56.

COMMENTS

The references cited in this Information Disclosure Statement ("IDS") were cited in patent infringement litigations in Korea and the United States involving Korean and U.S. patents that claim priority to the same U.S. parent application (US serial no. 08/554,902, now US Patent No. 5,974,662) as the above-identified patent application. These references were identified by one or more of the Defendants in those litigations as alleged grounds for invalidating one or more claims of the patents-in-suit.

The two U.S. litigations in which these references were cited are: (1) FormFactor, Inc. v. Phicom Corporation, United States District Court District of Oregon (Case No. 05-6062-HO) and (2) FormFactor, Inc. v. Micronics Japan Co. and MJC Electronics Corp., United States District Court,

Northern District Of California (Case No. 3:06-CV-07159 JSW). Citation No. 308 in the following listing of references is a copy of invalidity contentions of the Defendant Phicom in the former one of the two preceding litigations, and Citation No. 309 in the following listing of references is a copy of invalidity contentions of the Defendants Micronics Japan and MJC Electronics in the later one of the two preceding litigations. Patents that are at issue in the foregoing litigations include US Patent Nos. 5,974,662; 6,246,247; 6,624,648; 6,615,485; 5,994,152; 7,703,254; and 6,509,751. All of the foregoing patents are commonly owned and related to the above-identified patent application.

REFERENCES THAT MAY BE OF PARTICULAR NOTE

Applicants' representative has reviewed all of the references cited in this IDS and identifies the following citations as possibly being of particular note:

- US Patent No. 4,251,772 (Citation No. 13)
- US Patent No. 4,774,462 (Citation No. 33)
- US Patent No. 4,864,227 (Citation No. 39)
- US Patent No. 4,892,122 (Citation No. 42)
- US Patent No. 5,172,053 (Citation No. 85)
- US Patent No. 5,410,259 (Citation No. 126)
- US Patent No. 5,422,574 (Citation No. 131)
- US Patent No. 5,657,394 (Citation No. 197)
- US Patent No. 5,758,537 (Citation No. 211)
- US Patent No. 5,872,459 (Citation No. 231)
- JP 04-361543 (Citation No. 265)
- JP 06-027144 (Citation No. 267)
- JP 61-272946 (Citation No. 274)
- "Probe Card Stiffener With Theta Adjust," IBM Technical Disclosure Bulletin (Citation No. 290)

It is possible, however, that the Examiner could deem other references more relevant. It is therefore NOT Applicants' or Applicants' representative's intention to suggest that the foregoing are necessarily the most relevant references cited in the attached listing.

EXPLANATION OF FOREIGN LANGUAGE REFERENCE(S)

Citation No. 257 (FR 2639154) is in French. The following is an English translation of the Abstract, which Applicants provide as a concise summary of the reference: "The subject of the invention is a connector converting a specific integrated-circuit package with connection pads into a

standard package with connection pins of the PGA (Pin Grid Array) type. This connector includes: a multilayer base 30 carrying, on its lower side, a standard-package array of connection pins 35 and, on its upper side, an array of lands (metallised bonding areas) 37, arranged in the same way as the connection pads 17 of the specific package 10, together with conducting tracks for interconnecting the pins 35 and the lands (metallised bonding areas) 37, a multilayer intermediate board 40 for patching (interconnecting) the connections, an adjustment (setting) board 50 with an array of plated-through (metallised-wall) holes 51 serving as cages having contact springs 52, arranged in the same way as the connection pads 17 of the specific package 10, and a frame 60 for positioning the specific package 10."

- ☒ This statement qualifies under 37 C.F.R. §1.97, subsection (b) because (check all that apply):
- ☐ It is being filed within 3 months of the application filing date (other than the filing date of a continued prosecution application under §1.53(d)); or
 - ☐ It is being filed within 3 months of entry of a national stage; or
 - ☐ It is being filed before the mail date of the first Office Action on the merits; or
 - ☒ It is being filed before the mail date of the first Office Action after the filing of a request for continued examination under §1.114.
- ☐ 37 C.F.R. §1.97(c). If this statement is being filed three months after the filing date of a national application or entry of the national stage in an international application and the mailing date of a first Office action on the merits but before the mailing date of the earlier of a final office action, a notice of allowance, or an action that otherwise closes prosecution in the application, then:
- ☐ a statement as specified in §1.97(e) is provided below; **or**
 - ☐ a fee of \$180.00 as set forth in §1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.
- ☐ 37 C.F.R. §1.97(d). If this statement is being filed after the mailing date of the earlier of a final office action under §1.113, a notice of allowance under §1.311, or an action that otherwise closes prosecution in the application, but before payment of the issue fee, then:
- a statement as specified in §1.97(e) is provided below; **and**
 - a fee of \$180.00 as set forth in §1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.

Fee:

- ☒ No fee is due.
- ☐ A check that includes \$_____ for the above-identified fee(s) is enclosed.
- ☐ Payment is provided by credit card.
- ☐ This statement is filed as one part of a multi-part submission. The fee for the complete submission was included in an electronically filed submission on __. Applicants request the foregoing fee be accepted for this statement. (See OG Notice dated 17 September 2002.)

Respectfully submitted,

Date: November 1, 2007

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